Class	Subclass	ISSUE CLASSIFICATION
257	639	

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U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED DW Q.A. he	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/559903	D	438		2825 2815	ECKERT

APPLICANTS

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Richard Holscher

TITLE

Semiconductor processing methods, semiconductor circuitry, and gate stacks

PTO-204L
12/99

ISSUING CLASSIFICATION

ORIGINAL CLASS	ORIGINAL SUBCLASS	CROSS REFERENCE(S) CLASS	CROSS REFERENCE(S) SUBCLASS (ONE SUBCLASS PER BLOCK)					
257	639	257	412	413	437	649		

INTERNATIONAL CLASSIFICATION

H01L 23/58

☐ Continued on Issue Slip inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	3	6	6	5	1

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

(Assistant Examiner) (Date)

NOTICE OF ALLOWANCE MAILED

1-13-04

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

GEORGE ECKERT
PRIMARY EXAMINER
(Primary Examiner) 1/9/04 (Date)

ISSUE FEE

Amount Due	Date Paid
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☐ The terminal ___ months of this patent have been disclaimed.

Surle 1/14/4
(Legal Instruments Examiner) (Date)

ISSUE BATCH NUMBER

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